

# Chip Level Reverse Engineering

#### Initial Search

Initial literature search to identify target products and create data sheets

## Top-level Photo

We decap the chip and take surface photos with a Scanning Electron Microscope, then analyze the pictures.

## Circuit Extraction

We isolate specific circuits within chips and recreate their schematics them for further analysis

## Electrical Measurements

Input/output dynamics are measured with variable dynamics on the target chip

**Evidence Of Use Charts** 

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